############################################################################

# VGA

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#NET "HSync" LOC = P93 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "VSync" LOC = P92 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Blue[1]" LOC = P98 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Blue[0]" LOC = P96 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Green[2]" LOC = P102 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Green[1]" LOC = P101 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Green[0]" LOC = P99 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Red[2]" LOC = P105 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Red[1]" LOC = P104 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Red[0]" LOC = P103 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

############################################################################

# Micro SD Card

############################################################################

#NET "CLK" LOC = P57 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "DAT0" LOC = P83 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "DAT1" LOC = P82 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "DAT2" LOC = P90 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "DAT3" LOC = P85 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "CMD" LOC = P84 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

############################################################################

# Audio

############################################################################

#NET "AUDIO\_L" LOC = P88 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "AUDIO\_R" LOC = P87 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

############################################################################

# Seven Segment Display

############################################################################

#NET "SevenSegment[7]" LOC = P114 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "SevenSegment[6]" LOC = P110 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "SevenSegment[5]" LOC = P111 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "SevenSegment[4]" LOC = P112 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "SevenSegment[3]" LOC = P113 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "SevenSegment[2]" LOC = P115 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "SevenSegment[1]" LOC = P116 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "SevenSegment[0]" LOC = P117 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Enable[3]" LOC = P124 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Enable[2]" LOC = P121 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "Enable[1]" LOC = P120 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

############################################################################

# LED

############################################################################

#NET "LED[8]" LOC = P46 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "LED[7]" LOC = P47 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "LED[6]" LOC = P48 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "LED[5]" LOC = P49 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "LED[4]" LOC = P50 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "LED[3]" LOC = P51 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "LED[2]" LOC = P54 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

#NET "LED[1]" LOC = P55 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST ;

############################################################################

# DP Switches

############################################################################

#NET "DPSwitch[8]" LOC = P70 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "DPSwitch[7]" LOC = P69 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "DPSwitch[6]" LOC = P68 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "DPSwitch[5]" LOC = P64 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "DPSwitch[4]" LOC = P63 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW

= FAST | PULLUP ;

#NET "DPSwitch[3]" LOC = P60 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "DPSwitch[2]" LOC = P59 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "DPSwitch[1]" LOC = P58 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

############################################################################

# Switches

############################################################################

#NET "Switch[1]" LOC = P80 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "Switch[2]" LOC = P79 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "Switch[3]" LOC = P78 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "Switch[4]" LOC = P77 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "Switch[5]" LOC = P76 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "Switch[6]" LOC = P75 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

############################################################################

# GPIO

############################################################################

# HEADER P1

#NET "gpio\_P1[0]" LOC = P31 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P1[1]" LOC = P32 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P1[2]" LOC = P28 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P1[3]" LOC = P30 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P1[4]" LOC = P27 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P1[5]" LOC = P29 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P1[6]" LOC = P24 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P1[7]" LOC = P25 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

# HEADER P6

#NET "gpio\_P6[8]" LOC = P19 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW

= FAST | PULLUP ;

#NET "gpio\_P6[9]" LOC = P21 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P6[10]" LOC = P18 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P6[11]" LOC = P20 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P6[12]" LOC = P15 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P6[13]" LOC = P16 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P6[14]" LOC = P12 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P6[15]" LOC = P13 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

# HEADER P2

#NET "gpio\_P2[16]" LOC = P10 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P2[17]" LOC = P11 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P2[18]" LOC = P7 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P2[19]" LOC = P8 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P2[20]" LOC = P3 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P2[21]" LOC = P5 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P2[22]" LOC = P4 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P2[23]" LOC = P6 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

# HEADER P4

#NET "gpio\_P4[24]" LOC = P141 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW

= FAST | PULLUP ;

#NET "gpio\_P4[25]" LOC = P143 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P4[26]" LOC = P138 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P4[27]" LOC = P139 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P4[28]" LOC = P134 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P4[29]" LOC = P135 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P4[30]" LOC = P130 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P4[31]" LOC = P132 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

# HEADER P5

#NET "gpio\_P5[1]" LOC = P125 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW

= FAST | PULLUP ;

#NET "gpio\_P5[2]" LOC = P123 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P5[3]" LOC = P127 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P5[4]" LOC = P126 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P5[5]" LOC = P131 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P5[6]" LOC = P91 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P5[7]" LOC = P142 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

#NET "gpio\_P5[8]" LOC = P140 | IOSTANDARD = LVTTL | DRIVE = 8 |

SLEW = FAST | PULLUP ;

############################################################################

# GPIO

############################################################################

# HEADER P1

#NET "gpio\_P1[0]" LOC = P31 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P1[1]" LOC = P32 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P1[2]" LOC = P28 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P1[3]" LOC = P30 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P1[4]" LOC = P27 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P1[5]" LOC = P29 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P1[6]" LOC = P24 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P1[7]" LOC = P25 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

# HEADER P6

#NET "gpio\_P6[8]" LOC = P19 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P6[9]" LOC = P21 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P6[10]" LOC = P18 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P6[11]" LOC = P20 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P6[12]" LOC = P15 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P6[13]" LOC = P16 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P6[14]" LOC = P12 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P6[15]" LOC = P13 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

# HEADER P2

#NET "gpio\_P2[16]" LOC = P10 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP;

#NET "gpio\_P2[17]" LOC = P11 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP;

#NET "gpio\_P2[18]" LOC = P7 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P2[19]" LOC = P8 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P2[20]" LOC = P3 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P2[21]" LOC = P5 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P2[22]" LOC = P4 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P2[23]" LOC = P6 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

# HEADER P4

#NET "gpio\_P4[24]" LOC = P141 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P4[25]" LOC = P143 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P4[26]" LOC = P138 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P4[27]" LOC = P139 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P4[28]" LOC = P134 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P4[29]" LOC = P135 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P4[30]" LOC = P130 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P4[31]" LOC = P132 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

# HEADER P5

#NET "gpio\_P5[1]" LOC = P125 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P5[2]" LOC = P123 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P5[3]" LOC = P127 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P5[4]" LOC = P126 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P5[5]" LOC = P131 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P5[6]" LOC = P91 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P5[7]" LOC = P142 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;

#NET "gpio\_P5[8]" LOC = P140 | IOSTANDARD = LVTTL | DRIVE = 8 | SLEW = FAST | PULLUP ;